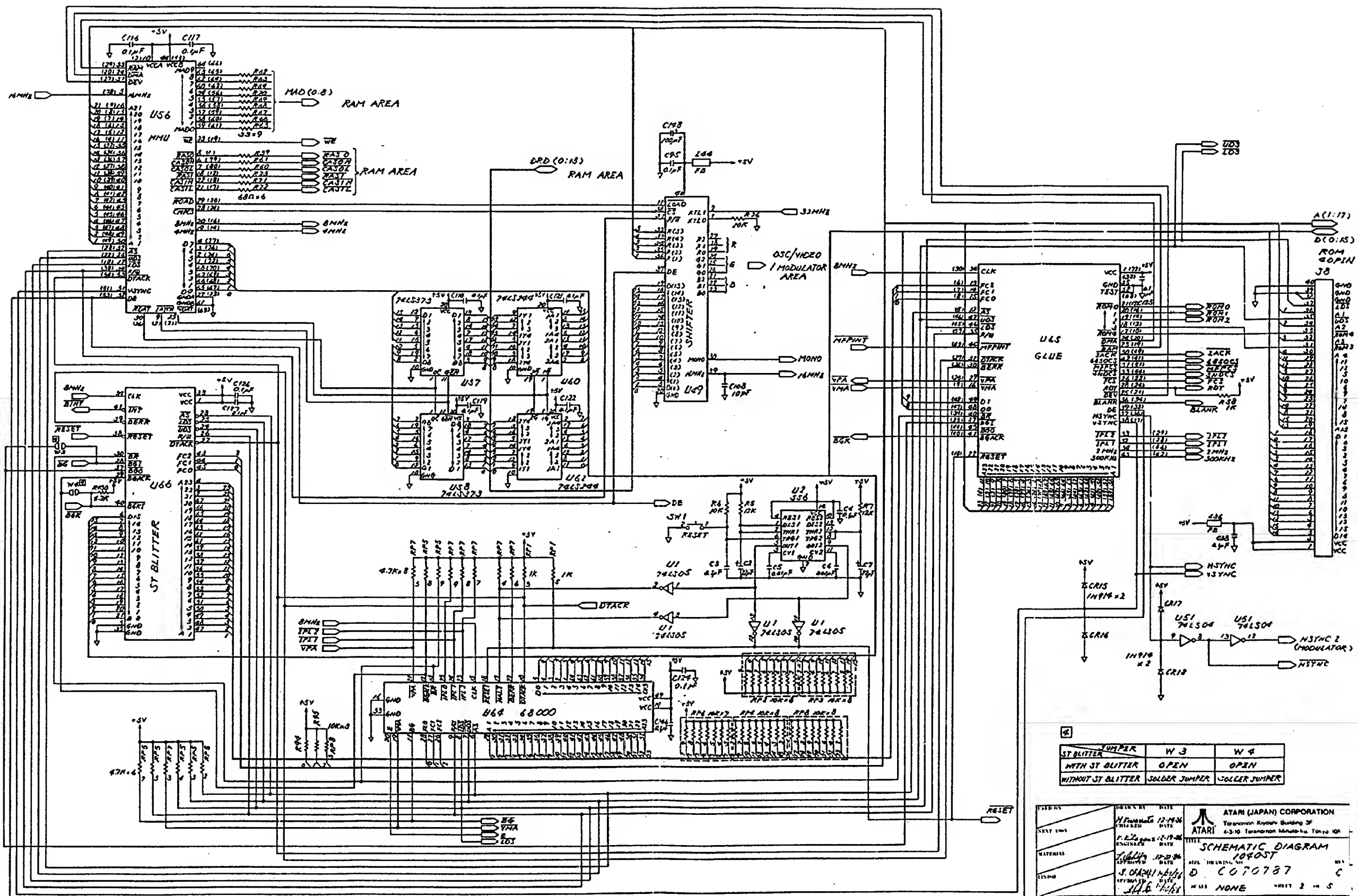
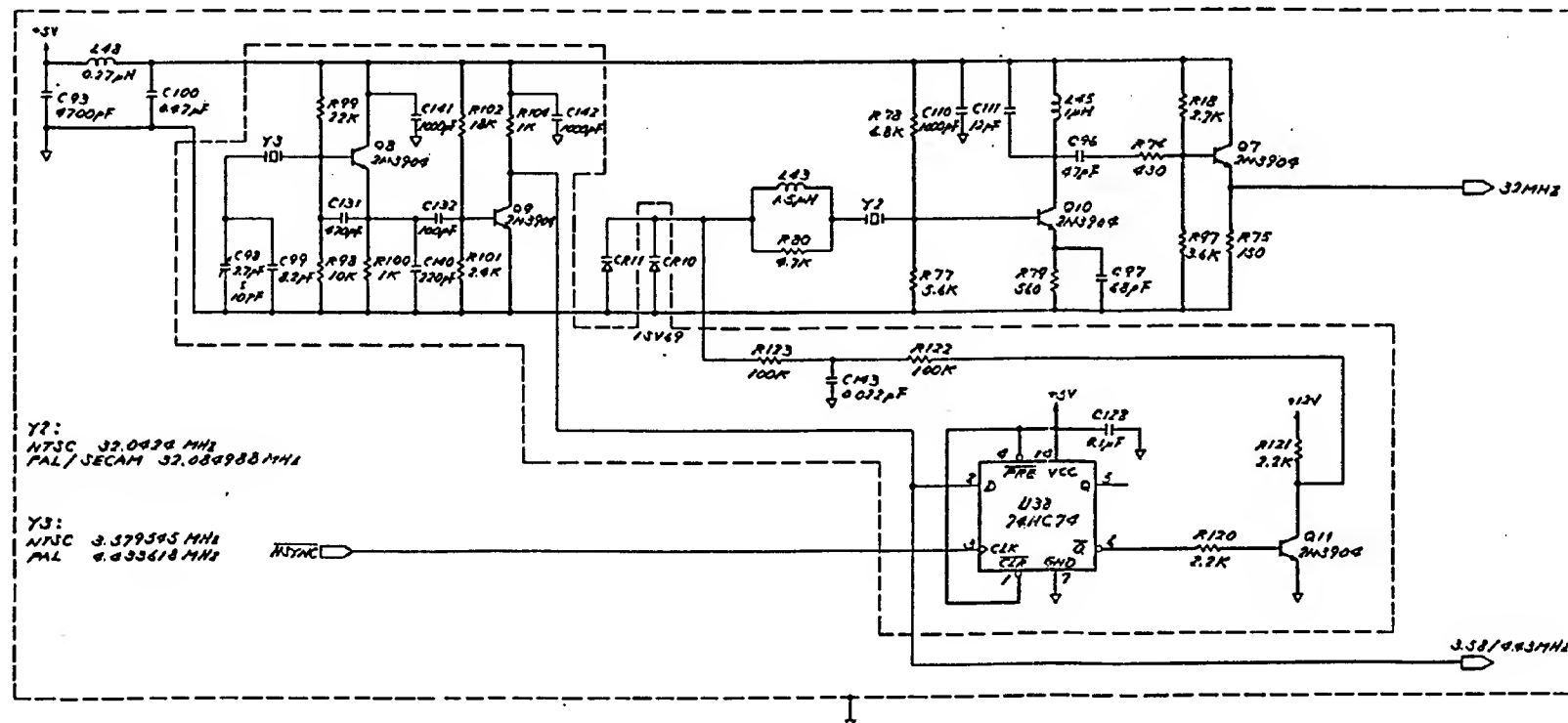
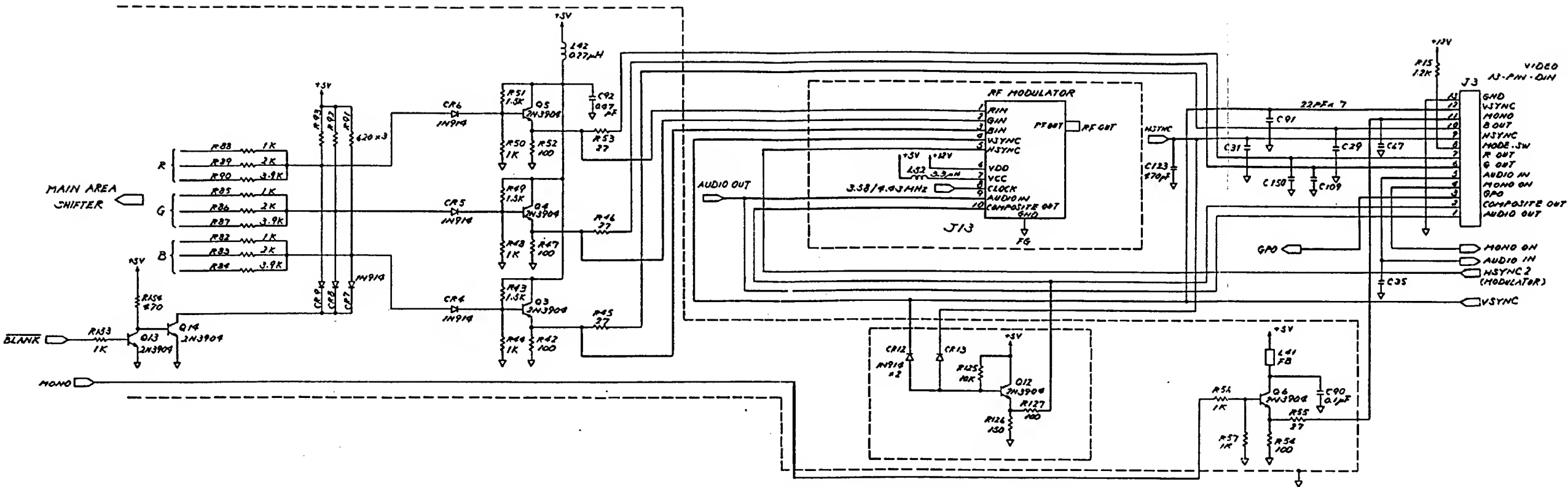


3. NOISE FILTER : SEE DETAIL 2
 2. RESISTORS ARE MEASURED IN OHMS, 5%, 1/4 WATT.
 1. CAPACITORS ARE MEASURED IN MICROFARADS.
 NOTES - UNLESS OTHERWISE SPECIFIED:



ST BLITTER JUMPER	W 3	W 4
WITH ST BLITTER	OPEN	OPEN
WITHOUT ST BLITTER	SOLDER JUMPER	SOLDER JUMPER

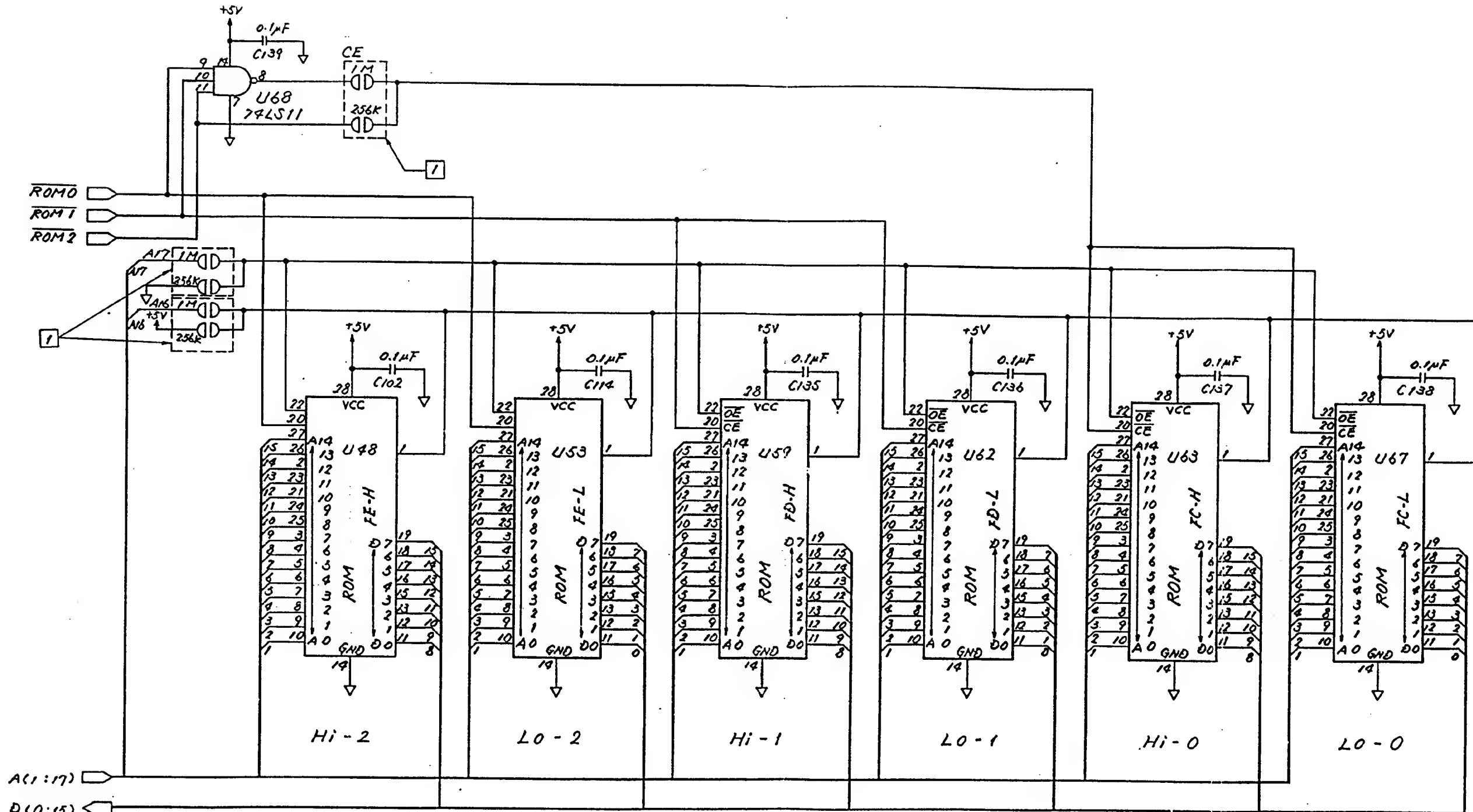
DATE	12-19-86	ATARI (JAPAN) CORPORATION
DESIGNED BY	Y. KAWA	Tokyo, Japan
ENGINEER	Y. KAWA	ATARI
DATE	12-19-86	SCHEMATIC DIAGRAM
APPROVED	Y. KAWA	10000
DATE	12-19-86	D. C070787
REVISION	1.0	NONE
SHEET	2	5



Y2:
NTSC 32.0430 MHz
PAL/SECAM 32.08988 MHz

Y3:
NTSC 3.579545 MHz
PAL 3.575818 MHz

DESIGNED BY	DATE	ATARI (JAPAN) CORPORATION
CHECKED BY	DATE	Torayama Kyosha Building
ENGINEER	DATE	4-3-10, Torayama Manasshu, Tokyo 100
TITLE	DATE	SCHMATIC DIAGRAM
1040ST	DATE	1040ST
APPROVED BY	DATE	DRAWING NO.
APPROVED BY	DATE	D 1040787
APPROVED BY	DATE	REVISION
APPROVED BY	DATE	NO. 3

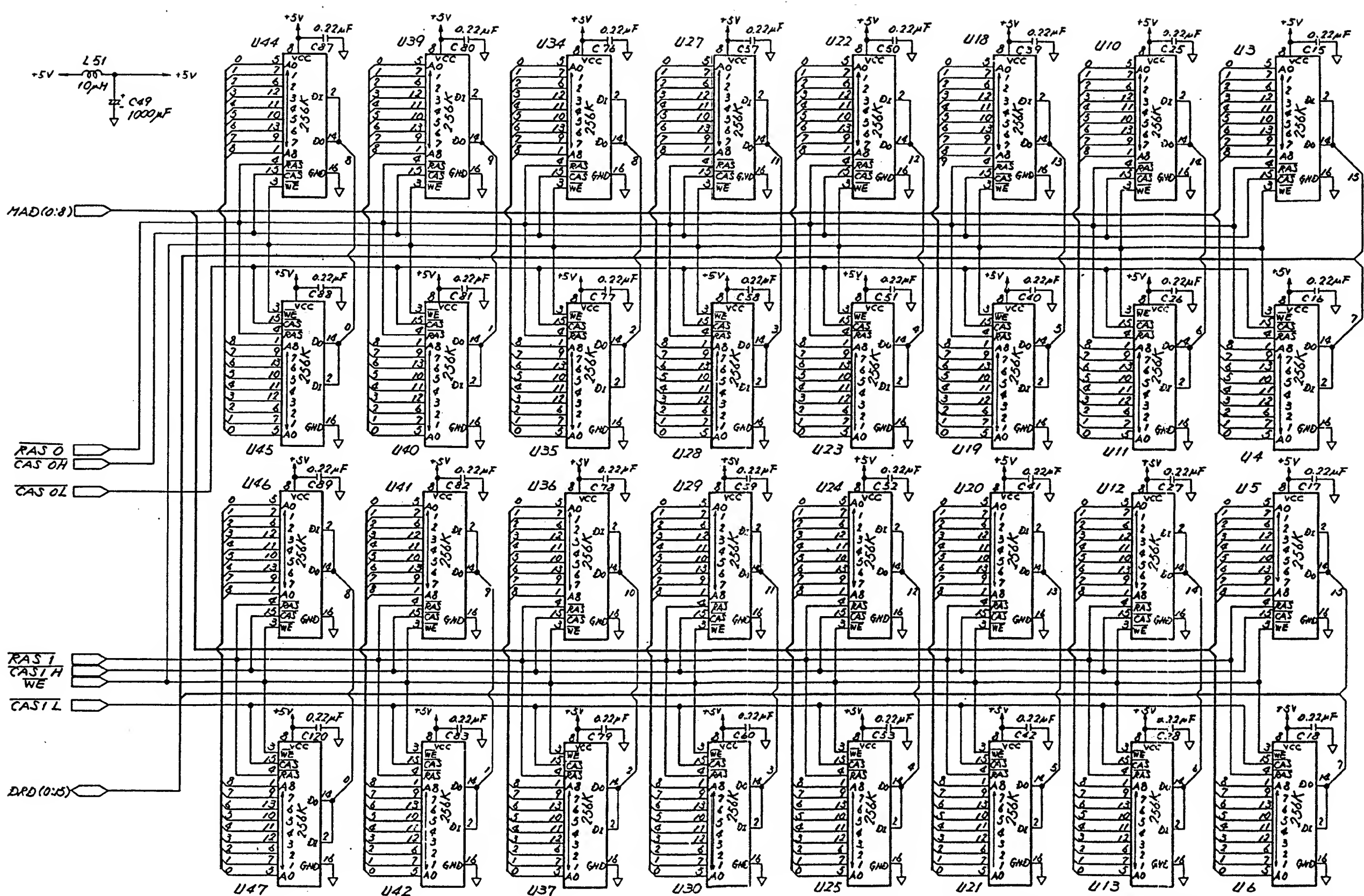


U48, U53;

PIN NO.	PIN ASSIGNMENTS	
	256 K	1 M
22	\overline{OE}	A17
1	NC	A16
20	\overline{CE}	\overline{CE}

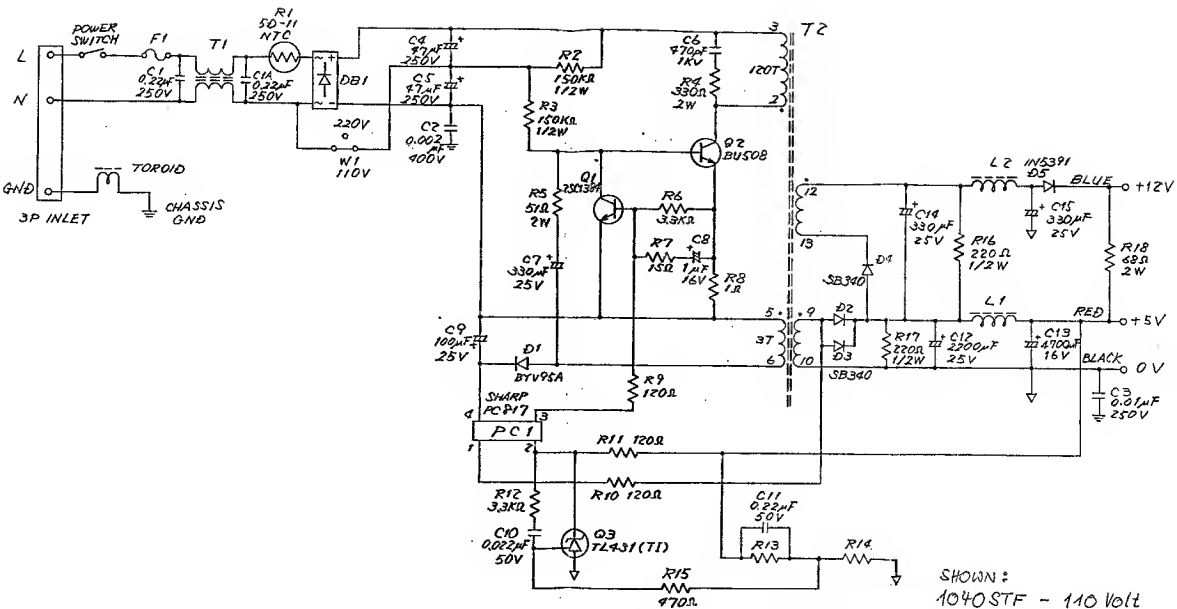
REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		

USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 4-3-10, Toranomon Minato-ku, Tokyo 105	
NEXT ASSY	CHECKED	DATE	TITLE SCHEMATIC DIAGRAM 1040ST	
MATERIAL	ENGINEER	DATE	SIZE	DRAWING NO.
FINISH	APPROVED	DATE	C	C070787
	APPROVED	DATE	SCALE	SHEET 4 OF 5



REV	REVISIONS DESCRIPTION	DATE	APPROVE
	SEE SHEET 1.		

USED ON	DRAWN BY H. KAWAMOTO 12-14-86	DATE 12-14-86	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 4-3-10, Toranomon Minato-ku, Tokyo 105	
NEXT ASSY	CHECKED K. K. YAMAGUCHI 12-22-86	DATE 12-22-86	TITLE SCHEMATIC DIAGRAM 1080ST	
MATERIAL	ENGINEER S. OKAZAKI 12/22/86	DATE 12/22/86	SIZE C	DRAWING NO. C070787
FINISH	APPROVED S. OKAZAKI 12/22/86	DATE 12/22/86	SCALE NONE	REV C
				SHEET 5 OF 5



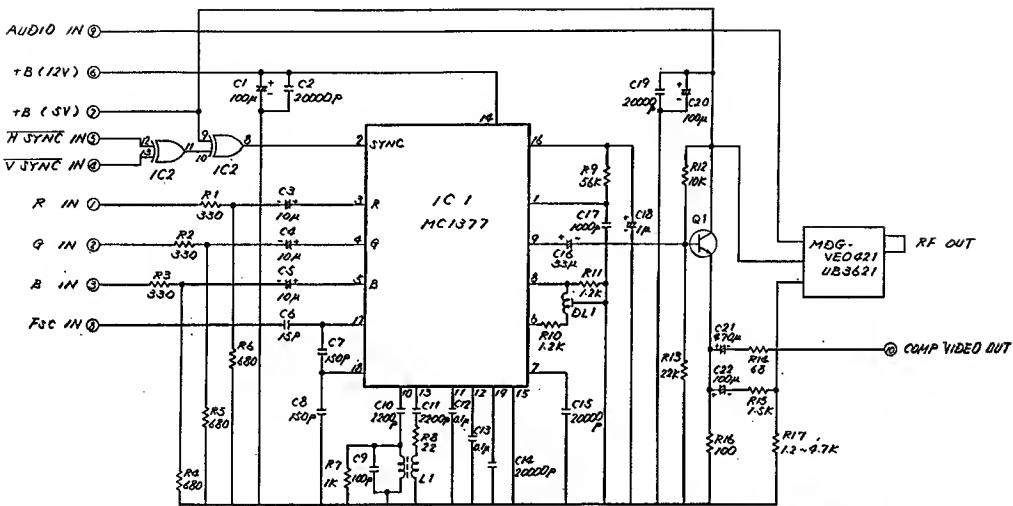
SHOWN:
1040STF - 110 Volt

REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	7/20/87	[Signature]

TOLERANCES	
UNDER 30	±0.1
30 THRU 300	±0.2
OVER 300	±0.4

USED ON ASP34-1	DATE 4/23/86
NEXT ASSY	DATE
ENGINEER K. K. [Signature]	DATE 7-8-86
APPROVED A. Shinagawa	DATE 10-23-86
FINISH	DATE
APPROVED [Signature]	DATE 1/23/87

ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 3F 4-3-10, Toranomon Minato-ku, Tokyo 105	
TITLE SCHEMATIC DIAGRAM (ASP34-1)	
SIZE B	DRAWING NO. C070798
REV A	
SCALE NONE	SHEET 1 OF 1



4. COMPONENT PARTS VALUE : $R = \Omega$, $C = F$
 3. Q1 : 2SC1684 OR EQUIVALENT
 2. IC2 : DN74LS86 OR EQUIVALENT
 1. IC1 : MC1377 OR EQUIVALENT
 NOTES - UNLESS OTHERWISE SPECIFIED:

TOLERANCES		USED ON	DRAWN BY	DATE
UNDER 30	± 0.1		K. Kawanishi	6-11-86
30 THRU 300	± 0.2		K. Kawanishi	6-11-86
OVER 300	± 0.4		K. Kawanishi	6-11-86

NEXT ASST		CHECKED	DATE
		K. Kawanishi	6-11-86

MATERIAL		APPROVED	DATE
		K. Kawanishi	6-11-86

FINISH		APPROVED	DATE
		K. Kawanishi	6-11-86

ATARI (JAPAN) CORPORATION		Toranomon Kiyoshi Building 3F	
4-3-10, Toranomon Minato-ku, Tokyo 105 <td colspan="2">TITLE</td>		TITLE	
RGB/ENCODER MODULATOR <td colspan="2">SIZE</td>		SIZE	
DRAWING NO.		REV	
B		A	
C070518		SCALE NONE	
SHEET 8 OF 10			